

Amendments to the Specification

Please amend paragraph [0001] as follows:

[0001] This application is a divisional of U.S. Patent Application No. 09/968,420, filed October 01, 2001, entitled "TEST TOOL AND METHODS FOR FACILITATING TESTING OF DUPLEXED COMPUTER FUNCTIONS", published on April 3, 2003 as U.S. Publication No. 2003-0065977 A1, the entirety of which is hereby incorporated herein by reference.

Please amend paragraph [0003] as follows:

[0003] "COUPLING OF A PLURALITY OF COUPLING FACILITIES USING PEER LINKS", Brooks et al., (~~IBM Docket No. POU920010039US1~~), Serial No. 09/968,244, filed October 01, 2001, published on October 16, 2003 as U.S. Publication No. 2003-0196016 A1;

Please amend paragraph [0004] as follows:

[0004] "RESTARTING A COUPLING FACILITY COMMAND USING A TOKEN FROM ANOTHER COUPLING FACILITY COMMAND", ~~Elko et al., (IBM Docket No. POU920010037US1), Serial No. 09/968,729, filed October 01, 2001~~ Dahlen et al., U.S. Patent No. 6,813,726 B2, issued November 2, 2004;

Please amend paragraph [0005] as follows:

[0005] "DYNAMICALLY DETERMINING WHETHER TO PROCESS REQUESTS SYNCHRONOUSLY OR ASYNCHRONOUSLY", Jordan et al., (~~IBM Docket No. POU920010043US1~~), Serial No. 09/968,185, filed October 01, 2001, published on April 3, 2003 as U.S. Publication No. 2003-0065709 A1;

Please amend paragraph [0006] as follows:

[0006] “MANAGING THE STATE OF COUPLING FACILITY STRUCTURES”, Elko et al., (~~IBM Docket No. POU920010041US1~~), Serial No. 09/968,248, filed October 01, 2001, published on October 16, 2003 as U.S. Publication No. 2003-0196071 A1;

Please amend paragraph [0007] as follows:

[0007] “SYNCHRONIZING PROCESSING OF COMMANDS INVOKED AGAINST DUPLEXED COUPLING FACILITY STRUCTURES”, Elko et al., (~~IBM Docket No. POU920010040US1~~), Serial No. 09/968,179, filed October 01, 2001, published on October 16, 2003 as U.S. Publication No. 2003-0196025 A1;

Please amend paragraph [0008] as follows:

[0008] “SYSTEM-MANAGED DUPLEXING OF COUPLING FACILITY STRUCTURES”, Allen et al., (~~IBM Docket No. POU920010042US1~~), Serial No. 09/968,242, filed October 01, 2001, published on April 3, 2003 as U.S. Publication No. 2003-0065971 A1;

Please amend paragraph [0009] as follows:

[0009] “METHOD, SYSTEM AND PROGRAM PRODUCTS FOR PROVIDING USER-MANAGED DUPLEXING OF COUPLING FACILITY CACHE STRUCTURES”, Elko et al., (~~IBM Docket No. PO9-98-036~~), Serial No. ~~09/255,382, filed February 22, 1999~~ U.S. Patent No. 6,539,495 B1, issued March 25, 2003;

Please amend paragraph [0010] as follows:

[0010] “CASTOUT PROCESSING FOR DUPLEXED CACHE STRUCTURES”, Elko et al., (~~IBM Docket No. PO9-99-032~~), Serial No. ~~09/255,383~~, filed ~~February 22, 1999~~ U.S. Patent No. 6,438,654 B1, issued August 20, 2002;

Please amend paragraph [0011] as follows:

[0011] “SYSTEM-MANAGED REBUILD OF COUPLING FACILITY STRUCTURES”, Allen et al., (~~IBM Docket No. PO9-99-096~~), Serial No. ~~09/378,780~~, filed ~~August 23, 1999~~ U.S. Patent No. 6,266,783 B1, issued July 24, 2001;

Please amend paragraph [0012] as follows:

[0012] “METHOD, SYSTEM AND PROGRAM PRODUCTS FOR COPYING COUPLING FACILITY STRUCTURES”, ~~Allen et al.~~, (~~IBM Docket No. PO9-99-138~~), Serial No. ~~09/379,054~~, filed ~~August 23, 1999~~ Dahlen et al., U.S. Patent No. 6,609,214 B1, issued August 19, 2003;

Please amend paragraph [0013] as follows:

[0013] “METHOD, SYSTEM AND PROGRAM PRODUCTS FOR MODIFYING COUPLING FACILITY STRUCTURES”, Dahlen et al., (~~IBM Docket No. PO9-99-137~~), Serial No. ~~09/379,435~~, filed ~~August 23, 1999~~ U.S. Patent No. 6,594,667 B2, issued July 15, 2003;

Please amend paragraph [0014] as follows:

[0014] “DIRECTED ALLOCATION OF COUPLING FACILITY STRUCTURES”, Dahlen et al., (~~IBM Docket No. PO9-99-097~~), Serial No. ~~09/378,861~~, filed ~~August 23, 1999~~ U.S. Patent No. 6,584,554 B1, issued June 24, 2003;

Please amend paragraph [0015] as follows:

[0015] “METHOD, SYSTEM AND PROGRAM PRODUCTS FOR COPYING COUPLING FACILITY LOCK STRUCTURES”, ~~Allen et al., (IBM Docket No. PO9-99-139), Serial No. 09/379,053, filed August 23, 1999~~ Dahlen et al., U.S. Patent No. 6,546,414 B1, issued April 8, 2003;

Please amend paragraph [0016] as follows:

[0016] “METHOD OF CONTROLLING THE FLOW OF INFORMATION BETWEEN SENDERS AND RECEIVERS ACROSS LINKS BEING USED AS CHANNELS”, Gregg et al. ~~(IBM Docket No. PO9-98-109), Serial No. 09/151,051, filed September 10, 1998~~ U.S. Patent No. 6,681,254 B1, issued January 20, 2004;

Please amend paragraph [0017] as follows:

[0017] “SYSTEM OF CONTROLLING THE FLOW OF INFORMATION BETWEEN SENDERS AND RECEIVERS ACROSS LINKS BEING USED AS CHANNELS”, Gregg et al. ~~(IBM Docket No. PO9-98-124), Serial No. 09/150,942, filed September 10, 1998,~~ now Abandoned;

Please amend paragraph [0048] as follows:

[0048] “TEST TOOL AND METHODS FOR TESTING A SYSTEM-MANAGED DUPLEXED STRUCTURE”, Jones et al., ~~(IBM Docket No. POU920010038US3), Serial No. _____,~~ co-filed herewith Serial No. 10/078,077, filed February 15, 2002, published on April 3, 2003 as U.S. Publication No. 2003-0065981 A1;

Please amend paragraph [0049] as follows:

[0049] “TEST TOOL AND METHODS FOR TESTING A COMPUTER FUNCTION EMPLOYING A MULTI-SYSTEM TESTCASE”, Shaw et al., ~~(IBM Docket No. POU920010038US4), Serial No. _____, co-filed herewith~~ U.S. Serial No. 10/076,677, filed February 15, 2002, published on April 3, 2003 as U.S. Publication No. 2003-0065980 A1; and

Please amend paragraph [0050] as follows:

[0050] “TEST TOOL AND METHODS FOR TESTING A COMPUTER STRUCTURE EMPLOYING A COMPUTER SIMULATION OF THE COMPUTER STRUCTURE”, Scott B. Tuttle, ~~(IBM Docket No. POU920010038US5), Serial No. _____, co-filed herewith~~ U.S. Serial No. 10/078,194, filed February 15, 2002, published on April 3, 2003 as U.S. Publication No. 2003-0065975 A1.